Unit: mm

TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (π -MOSIV)

2SK4014

DC/DC Converter, Relay Drive and Motor Drive Applications

• Low drain-source ON-resistance : $R_{DS\ (ON)}$ = 1.6 $\Omega\ (typ.)$

High forward transfer admittance : |Y_{fs}| = 5.0 S (typ.)
 Low leakage current : I_{DSS} = 100 μA (max) (V_{DS} = 720 V)

Enhancement mode : V_{th} = 2.0~4.0 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteri	stic	Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	900	V
Drain-gate voltage (Ro	_{SS} = 20 kΩ)	V_{DGR}	900	V
Gate-source voltage		V _{GSS}	±30	V
Drain current	DC (Note 1)	I _D	6	Α
	Pulse (Note 1)	I _{DP}	18	Α
Drain power dissipation	n (Tc = 25°C)	P _D	45	W
Single-pulse avalanche	e energy (Note 2)	E _{AS}	972	mJ
Avalanche current		I _{AR}	6	Α
Repetitive avalanche energy (Note 3)		E _{AR}	15	mJ
Channel temperature		T _{ch}	150	°C
Storage temperature ra	ange	T _{stg}	-55~150	°C

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1. Gate
2: Drain
3: Source

JEDEC

JEITA

SC-67

TOSHIBA

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Weight: 1.7 (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

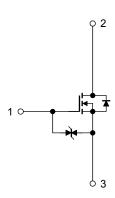
Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	2.78	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD} = 90 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), L = 49.5 mH, $R_G = 25 \Omega$, $I_{AR} = 6 \text{ A}$

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.





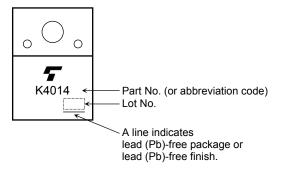
Electrical Characteristics (Ta = 25°C)

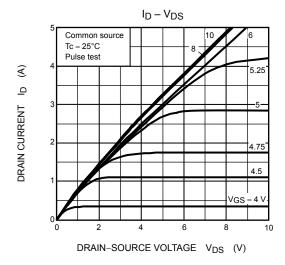
Charae	cteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I _{GSS}	V _{GS} = ±30 V, V _{DS} = 0 V	_	_	±10	μΑ
Gate-source bre	eakdown voltage	V (BR) GSS	I _G = ±10 μA, V _{DS} = 0 V	±30	_	_	V
Drain cutoff curr	ent	I _{DSS}	V _{DS} = 720 V, V _{GS} = 0 V	_	_	100	μΑ
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	900	_	_	V
Gate threshold v	voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	4.0	V
Drain-source O	N-resistance	R _{DS (ON)}	V _{GS} = 10 V, I _D = 3 A	_	1.6	2.0	Ω
Forward transfer	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 3 A	_	5.0	_	S
Input capacitano	e	C _{iss}			1400	_	pF
Reverse transfer capacitance		C _{rss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	_	30	_	
Output capacita	put capacitance C _{oss}		_	130	_		
Switching time	Rise time	t _r	$V_{GS} \xrightarrow{0V} \xrightarrow{I_D = 3 \text{ A}} V_{out}$ $50 \Omega \xrightarrow{R_L = 133 \Omega}$	_	25	_	
	Turn-on time	t _{on}		_	75	_	20
	Fall time	t _f		_	60	_	ns
	Turn-off time	t _{off}	$V_{DD} = 400 \text{ V}$ $Duty \leq 1\%, \ t_{\mathbf{W}} = 10 \mu s$		220	_	
Total gate charg plus gate-drain)		Qg			45		_
Gate-source charge		Q _{gs}	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 6 \text{ A}$		25	_	nC
Gate-drain ("Mil	ate-drain ("Miller") charge Q _{gd}		_	20	_		

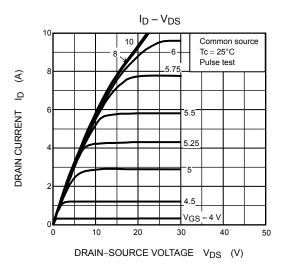
Source-Drain Ratings and Characteristics (Ta = 25°C)

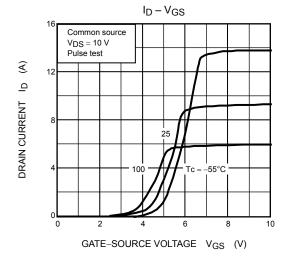
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	6	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	18	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 6 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 6 A, V _{GS} = 0 V	_	1100	_	ns
Reverse recovery charge	Q _{rr}	dl _{DR} / dt = 100 A / μs	_	10	_	μC

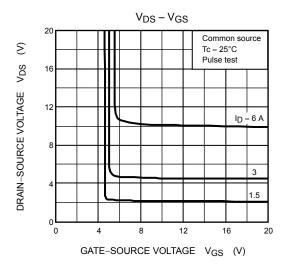
Marking

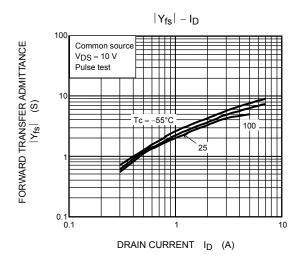


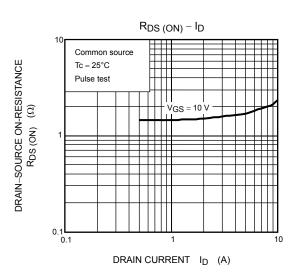




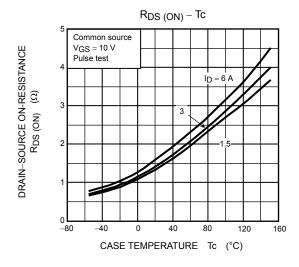


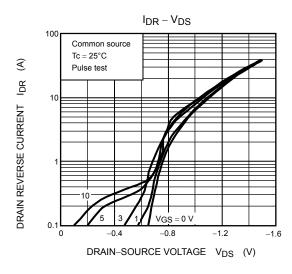


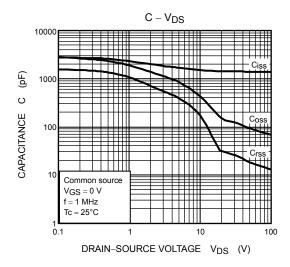


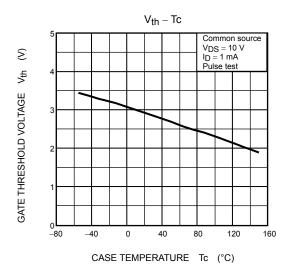


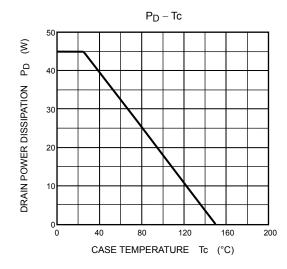
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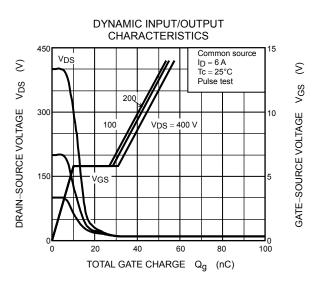


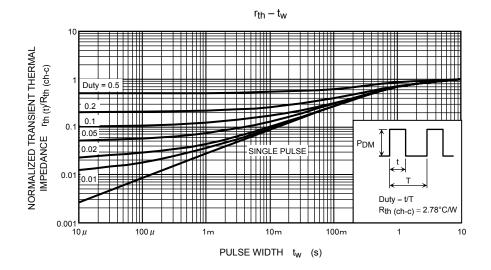


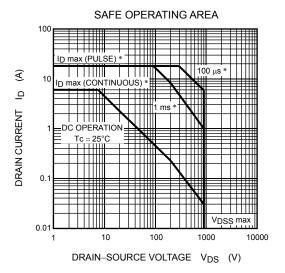


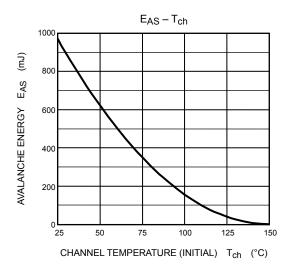


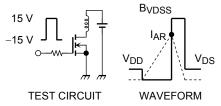












$$\begin{aligned} &R_G = 25~\Omega \\ &V_{DD} = 90~V,~L = 49.5~mH \end{aligned} \qquad \text{EAS} = \frac{1}{2} \cdot L \cdot l^2 \cdot \left(\frac{BVDSS}{BVDSS - V_{DD}} \right)$$

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20070701-EN

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